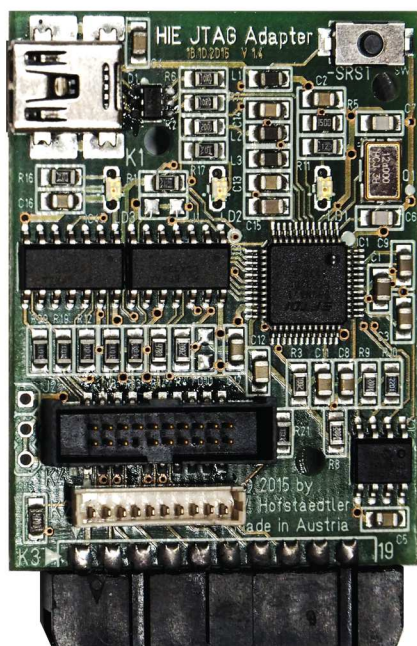


# HIE JTAG DEBUGGER

## A VERSATILE AND EASY TO USE JTAG DEBUGGER

The HIE JTAG adapter is a **FTDI® based USB JTAG debugger**. It provides **3 JTAG connectors**, the original 0,1" 20 pin connector, a 0,05" 20 pin connector and a 10 pin Molex Picoblade® connector **compatible with Engicam® JTAG connector**.



## FEATURE RICH:

- a Target Reset (-SRST) push button
- a red LED showing -SRST active
- a red LED for Target Power ON
- an orange LED to show USB activity
- **3 JTAG connectors** and a **150mm (1.27mm)** ribbon cable with **10 and 20 pin connectors** supports most targets
- **Universal target voltage support (1.8V) 2.5V to 5.0V**
- HIE JTAG uses an **USB 2.0 Hi-Speed FT232HL chip** with **MPSSE state machine**
- HIE JTAG is fast, **RTCK up to 15 MHz will work**, higher speed is possible

Host connection is a **USB 2.0 Mini USB type B** connector with **ESD protection**.

The **HIE JTAG debugger** has been tested with **OpenOCD 0.8.0** on **Linux and Windows**. HIE JTAG debugger will **support most CPUs** like **ARM®, MITPS®, X86®** and many more, a JTAG debugging port is required.

### Packing List:

1. one **HIE JTAG debugger board**, dimensions **55 x 36 mm**
2. one **ribbon cable**, length **150mm**, 3 connectors, 20/10 pin, **1.27mm 0.05"**

Designed and Made in Austria - [hofstaedtler.com](http://hofstaedtler.com)

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